STUDY THE EFFECT OF DIFFERENT LAYER ARRANGEMENT ON EFFICIENCY OF GALLIUM ARSENIDE SOLAR CELLS USING SILVACO TCAD TOOLS SOFTWARE

This thesis is presented in partial fulfillment for the award of the Bachelor of Engineering (Hons) in Electrical Engineering UNIVERSITI TEKNOLOGI MARA MALAYSIA



MEOR MOHD EKHWAN B MEOR IBRAHIM 2006130633 B. ENG (Hons.) ELECTRICAL Faculty of Electrical Engineering UNIVERSITI TEKNOLOGI MARA MALAYSIA (UiTM) Shah Alam, Selangor Darul Ehsan.

ACKNOWLEDGEMENT

All praises be to mighty Allah SWT the Most Gracious and Most Merciful for the strength and blessing throughout the entire research until the completion of this thesis. Peace is upon our Prophet Muhammad SAW whose has given light to mankind.

I would like to convey my heartfelt to my supervisor, Dr. Fuziah Sulaiman for her guidance, commitment, favor and support for this project. Without her, this project might not be done successfully.

A special appreciation to my parents and family, for their encouragement and support.

Moreover, I am greatly indebted to all my course mates and friends for their aid and comments throughout development of this project.

Last but not least, I would like to take this opportunity to express my appreciation to those that have directly or indirectly contributed towards the progress of this thesis.

ABSTRACT

Photovoltaics is a renewable energy technology that converts the sunlight directly into electricity through a photovoltaic (PVs) cell, commonly called a solar cell. Basically, they are made from semiconductor material such as silicon, gallium arsenide, amorphous and many more semiconductors. Photovoltaic energy has become popular nowadays because of its ability to provide nearly permanent, uninterrupted power. What is more interesting, there is no operating cost. This research was conducted to study the effect of different layer arrangement of GaAs p/n heteroface solar cell. Three different model of GaAs p/n heteroface solar cell structure were introduced and had been simulated. ATLAS device simulator by Silvaco International was introduced for used as a tool in modeling solar cell. This project was conducted only to design and simulate both solar cell based on virtual fabrication but not involves real fabrication. This ATLAS simulator extracts the electrical characteristics of a solar cell based on virtual fabrication of its physical structure, allowing for direct manipulation of materials, dimensions and dopings. The analysis focused on spectral response analysis. From spectral response analysis, two parameters were issue, photo current and efficiency. The final result shows that, different layer arrangement can effect on photo current and efficiency.

TABLE OF CONTENTS

LIST OF TITLE	PAGE
DECLARATION	i
ACKNOWLEGDEMENT	Ĩ
ABSTRACT	iii
TABLE OF CONTENTS	iv
LIST OF FIGURES	vi
LIST OF TABLES	vii
LIST OF ABREVIATIONS	viii

CHAPTER 1

INTRODUCTION				
1.1	BACKGROUND			
1.2	HISTORY	2		
1.3	OBJECTIVE	4		
1.4	SCOPE OF WORK	3		

CHAPTER 2

LITERATURE REVIEW

2.1	SEMICONDUCTOR PHYSICS	5
	2.1.1 Basic Semiconductor Model	5
	2.1.1.1 Bohr Model	5
	2.1.1.2 Energy Bands	8
2.2	P-N JUNCTION	9
2.3	HOW SOLAR CELL WORKS	10
2.4	MULTI-JUNCTION SOLAR CELLS	12
2.5	PERFORMENCE OF SOLAR CELL	13
	2.5.1 Open Circuit Voltage, V_{ac}	14
	2.5.2 Short circuit Current, I _{sc}	14
	2.5.3 Fill Factor, FF	14

CHAPTER 3

METHODO	DLOGY	
3.1	SILVACO SIMULATOR	16
3.2	MODELLING SOLAR CELL STRUCTURE	18
3.3	SIMULATION OF SOLAR CELL IN ATHENA	
	FRAMEWORK	20
3.4	SOLAR CELL ANALYSIS	21

CHAPTER 1

INTRODUCTION

1.1 BACKGROUND

Sun's energy is the source of nearly all energy on the earth. Humans, like all other animals and plants, rely on the sun for warmth and food. People harness the sun's energy in many other different ways. Photovoltaics is a simple and elegant method of harnessing the sun's energy. Photovoltaics devices are unique in that they directly convert the incident solar radiation into electricity, with no noise, pollution or moving parts, making them robust, reliable and long lasting[1].

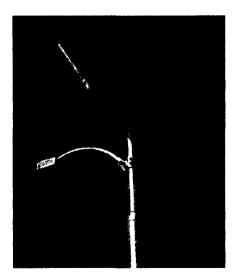


FIGURE 1.1: A solar-powered lamppost