# OPTIMAL METHOD OF REDUCING HARMONIC CONTENT FOR SINUSOIDAL PULSE WIDTH MODULATION (SPWM)

A thesis submitted in partial fulfillment of the requirements for the award of Bachelor of Engineering (Hons.) Electrical



MOHD DAS BIN RISIN FACULTY OF ELECTRICALENGINEERING 40450 SHAH ALAM, SELANGOR, UNIVERSITI TEKNOLOGI MARA MALAYSIA

**NOVEMBER 2009** 

#### **ACKNOWLEDGEMENT**

In the name of Allah S.W.T, The Most Beneficent, The Most Merciful. Foremost, all praise to Allah for the entire incredible gift endowed upon me for giving me the health and strength to complete this thesis and project

Firstly, I would like to this opportunity to express my most grateful appreciation to my supervisor, Puan Hanim Hussin who has supervised and support me. And then thank with millions and endless appreciation to my co-supervisor Dr Azilah Saparon, who was helping me understanding the project since the beginning and a lot of information about this project.

Herein, I would like to emphasize this thankfulness to my family members especially my parents Risin bin Gadu and for their advices, prayer, encouragement and continuous moral supports for the completion of my study in UiTM.

Special thanks to my lovely friends, Mohamad Zarifi b Mohd Rodzi, Nurul Hidayah binti Marzuki, Mohd Asyraf bin Rongi, Salehudin bin Mohd Redzuan, Nik Azman Nik Hassan, who were involved in the progression of this thesis and project.

Lastly, specially express gratitude to my colleagues, friends and lecturers of Faculty of Electrical Engineering, MARA University of Technology for their support towards the completion of this project. With all love and million thanks to all of these people.

Thank you.

## ABSTRACT

This project is focusing on optimizing harmonic content in Sinusoidal Pulse Width Modulation (SPWM) design. The results show that by varying number of bits, modulation index and switching frequency, the harmonic content to Total Harmonic Distortion (THD) can be analyzed and optimal method to reduce the harmonic content of THD can be found.

This SPWM is designed using VHDL and implemented on ALTERA (DE2-70 board). The results, the SPWM waveforms and its Fast Fourier Transform (FFT) waveform, are measured using Digital Oscilloscope.

The Total Harmonic Distortion (THD) is calculated from measured waveform. From the analysis, for 10 bit, mod index one and at frequency of 20 kHz, Total Harmonic Distortion for the SPWM design is optimized.

# **TABLE OF CONTENTS**

CONTENTS		PAGE
ACKNOWLEGEMENT		$\mathbf{v}$
ABSTRACT		vi
TABLE OF CONTENTS		vii
LIST OF TABLE		x
LIST OF FIGURE		xi
CHAPTER 1 INTRODUCTION		
1.0	Background of the project	1
1.1	Objective of the project	2
1.2	Problem identification	2
1.3	Scope of the project	3
CHAPTER 2 LITERATURE REVIEW		
2.0	Technique in generating SPWM	4
2.1	Harmonic in SPWM	6
2.2	Type of SPWM	7
2.3	Application of SPWM	8
	2.3.0 Encoded data	8
	2.3.1 Audio and amplifier system	9
	2.3.2 Power delivered	10

## **CHAPTER 1**

## **INTRODUCTION**

## 1.0 BACKGROUND OF THE PROJECT

Sinusoidal pulse width modulation is a method of pulse width modulation used in inverters. Pulse width modulation produced when the width of the pulse can be adjusted as a means of regulating the output voltage [1]. The pulses near the edges of the half cycle are always narrower than the pulses near the center of the half cycle such that the pulse widths are proportional to the corresponding amplitude of a sine wave at that portion of the cycle. To change the effective output voltage, the widths of all pulses are increased or decreased while maintaining the sinusoidal proportionality.

The harmonics in Sinusoidal Pulse width Modulation occurs during the inverter produces a square voltage waveform. And then using Fourier analysis, periodic waveforms are represented as the sum of an infinite series of sine waves. The sine wave that has same frequency as the original waveform is called as fundamental component and the other sine wave known as harmonics component [2].

Pulse width is normally used as a controller in power conversion and motion control. It also widely used for converting DC power to AC power in low power wind plants and also used in power electronics to digitize the power, that a sequence of alternating voltage pulse can be generated by the on and off of the inverter power transistor. [3], [4]. A control technique for the PWM converter is based on the input current control, in which the current commands are provided from the detect power-source voltages [5]. There are various kinds of modulation modes available such as sinusoidal PWM, current tracking PWM, space vector PWM and harmonic elimination [6].