MITIGATION OF HARMONIC DISTORTION USING SHUNT ACTIVE POWER FILTER

This thesis is presented in partial fulfillment for the award of the Bachelor of Electrical Engineering (Hons.)

of

UNIVERSITI TEKNOLOGI MARA MALAYSIA



MUHD KAMAL ASHRAF BIN MAT NASIR Faculty of Electrical Engineering UNIVERSITI TEKNOLOGI MARA 40450 SHAH ALAM, SELANGOR

ACKNOWLEDGEMENTS

In the name of Allah, the most Compassionate and the Most Merciful

I would like to express my sincere gratitude and endless thanks to my supervisor, Associate Profesor Wan Norainin Wan Abdullah for her guidance, encouragement, and continuous support throughout the completion of this thesis. Her extensive knowledge, advice and creative thinking have been an invaluable help to this research work.

I also would like to extend my appreciation towards lecturers at the Faculty of Electrical Engineering, Universiti Teknologi MARA (UiTM) for their assistance and cooperation. My thanks are also reserved to persons who involve in this project directly and indirectly towards completion of this thesis. Another group to be appreciated are my friends which have been continuously providing me help and guidance, whether in personal or formal matters.

Finally, my deepest gratitude and special thank you are for my family for their support, understanding, and encouragement.

MUHD KAMAL ASHRAF BIN MAT NASIR FACULTY OF ELECTRICAL ENGINEERING UNIVERSITI TECKNOLOGI MARA (UITM) SHAH ALAM, SELANGOR DARUL EHSAN

ABSTRACT

In recent years, the usage of power electronics equipments continues to increase, due to the increased usage of nonlinear loads and distributed power sources. These nonlinear loads draw non sinusoidal current from the mains to generate harmonics and reactive currents, which lead to low power factor, low energy efficiency, low power capacity, and harmful disturbance to other appliances. These harmonic and reactive currents will distort the voltage at the point of common coupling, reducing the quality of power delivered on the network. This paper presents a Shunt Active Power Filter (APF) which focuses on mitigating the harmonic distortion. The proposed method uses harmonics current injection to compensate the harmonics in the power line by injecting equal but opposite harmonic compensation current. An optimal injection current is achieved in order to obtain an input current of sinusoidal waveforms. The compensation current reference for the proposed topology is obtained by using fixed-band hysteresis current control. This work describes the design of circuit topology, control system, low pass filter and compensation current reference estimation. The performance of the Shunt APF is illustrated using PSIM simulation package.

TABLE OF CONTENTS

CHAPTER	CHAPTER DESCRIPTION				
		CKNOWLEDGEMENT	<u>PAGE</u>		
	i				
	ii				
	iii				
	v				
	vii				
	viii				
	ix				
1	1 INTRODUCTION				
	1.1	Introduction	1		
	1.2	Objectives	4		
	1.3	Scope of work	4		
	1.4	Thesis organization	5		
2	2 THEORETICAL BACKGROUND				
	2.1	Introduction	6		
	2.2	Harmonics	7		
	2.3	Sources of harmonics	8		
	2.4	Effects of harmonics	9		
	2.5	Meeting IEEE 519-1992 harmonic limits	9		
	2.6	Harmonic filtering techniques	11		
	2.7	Passive filter	12		
	2.8	Active filter	16		
		2.8.1 Shunt active power filter	18		
		2.8.2 Series active power filter	20		
	2.9	Reference signal estimation technique	21		
		2.9.1 Fourier Transform Techniques	22		
	2.10	Control techniques for active power filter	22		
		2.10.1 Hysteresis control technique	23		

3	METHODOLOGY						
	3.1	Introduction					
	3.2	Mitigation of harmonic distortion					
	3.3	Three phase power line without shunt APF					
		3.3.1	Supply and Load filter	28			
	3.4	Three phase power line with shunt APF					
	3.5	Power Circuit					
		3.5.1	Full bridge inverter	30			
		3.5.2	LCL Filter	31			
	3.6	Contro	ol Circuit	33			
		3.6.1	Hysteresis Band Current Controller	33			
		3.6.2	Reference Current Generator	35			
4	RES	RESULTS AND DISCUSSION					
	4.1	Introdu	action	36			
	4.2	Before	installing shunt APF	36			
	4.3	After in	nstalling shunt APF	39			
5	CON	CONCLUSION AND RECOMMENDATION					
	5.1	Conclu		44			
	5.2	Recomi	mendation	45			
	REFE	REFERENCES					
	APPE	NDICES	3				
	Appen	dix A-1:	Shunt APF Topologies Implemented with				
			PWM-VSI				
	Appen	dix A-2:	Three Phase Shunt APF to Compensate				
			The Harmonic Distortion				