

**SUBNETWORK AND TRANSMISSION PATH  
FOR VOLTAGE SECURITY REINFORCEMENT**

This report is present in partial fulfillment for the award of the  
Bachelor of Electrical Engineering (Honours)  
**UNIVERSITI TEKNOLOGI MARA (UiTM)**



**AZHAR BIN SHAHIDAN  
FACULTY OF ELECTRICAL ENGINEERING  
UNIVERSITI TEKNOLOGI MARA  
40450, SHAH ALAM, SELANGOR**

## **ACKNOWLEDGEMENT**

All praises be to Might ALLAH S.W.T, the Merciful and Beneficent for the strength and blessing me throughout the entire research and completion of this thesis.

I would like to express my sincere gratitude and appreciation to my supervisor, Dr Ismail Bin Musirin for his invaluable suggestion, guidance and constant encouragement during the preparation of this thesis.

I would also like to express my extraordinary appreciation to my family for their invaluable support along the duration of my studies until the completion of this thesis.

Finally, my sincere thanks to all my friend and other individuals for their support who helped me directly or indirectly in successful completion of my thesis.

*Azhar bin Shahidan*  
*Faculty of Electrical Engineering*  
*Universiti Teknologi MARA (UiTM)*  
*Shah Alam, Selangor Darul Ehsan*

## ABSTRACT

This thesis presents the formation of subnetwork and transmission path for voltage security reinforcement. The aim is to identify the weakest bus from the voltage stability point of view, the most loaded transmission path for that bus, again from the voltage security point of view. Once weak buses are found by the assessment function, enhancement controls actions may be recommended. In realizing the proposed technique, IEEE 30-bus system is used as the test specimen.

*Keywords-* voltage stability; voltage collapse; voltage security; power flow

## **TABLE OF CONTENTS**

<b><u>CHAPTER</u></b>	<b><u>DESCRIPTION</u></b>	<b><u>PAGE</u></b>
<b>1.</b>	<b>INTRODUCTION</b>	
	1.1. Introduction	1
	1.2. Project Objective	2
	1.3. Scope of Work	2
	1.4. Thesis Organization	2
<b>2.</b>	<b>LITERATURE REVIEW</b>	
	2.1. Introduction	4
	2.2. Power Flow Analysis	4
	2.2.1 Newton Raphson Method	6
	2.2.2 Power Flow Solution	8
	2.2.3 Power Flow Equation	9
	2.2.4 Newton Raphson Power Flow Solution	10
	2.2.5 Power Flow Program	14
	2.2.6 Data Preparation	16
	2.3. Voltage stability	19
	2.3.1. Voltage stability and voltage instability	19
	2.4 Theory of Fast Voltage Stability Index (FVSI)	21
	2.5 Weak Area Clustering Technique	23
<b>3.</b>	<b>METHODOLOGY</b>	
	3.1 Introduction	25
	3.2 Weak bus identification	25
	3.3 Transmission path identification	28
	3.4 Subnetwork identification	28

<b>CHAPTER</b>	<b>DESCRIPTION</b>	<b>PAGE</b>
<b>4.</b>	<b>RESULTS AND DISCUSSION</b>	
	4.1 Introduction	31
	4.2 Weakest bus at the maximum loadability	32
	4.3 Transmission path clustered	32
<b>5.</b>	<b>CONCLUSION</b>	37
<b>6.</b>	<b>REFERENCES</b>	38
<b>7.</b>	<b>APPENDICES</b>	40