## LOSS DUE TO MICROSTRIP BEND DISCONTINUITY IN MICROSTRIP LINE

Project report is presented in partial fulfillment for the award of the Bachelor of Electrical Engineering (Hons) UNIVERSITI TEKNOLOGI MARA



SHAHRUL FADZLY BIN ABU SEMAN Faculty of Electrical Engineering UNIVERSITI TEKNOLOGI MARA 40450 SHAH ALAM, SELANGOR

i

#### ACKNOWLEDGEMENTS

During the preparation of this project report, there was numerous numbers of peoples who made significant contributions. Thereby here, first of all, I would like to express my gratitude to God for enable me to complete my undergraduate degree program within two and a half year. I am deeply grateful to my supervisor, Miss Nor Ayu Zalina Zakaria for her leadership, energy, dedication, professionalism and for the continuing personal sacrifices she made for the past one year in order to ensure the quality, completeness and timeliness of this project. Nevertheless, my sincerest appreciation is indebted to Dr. Zaiki Awang for his great assistance along with his critical and useful suggestion. In fact, his generosity of providing the SMA connector and the assistance of him on the VNA calibration procedure is intensely helpful. I am also would like to take this opportunity to show my appreciation to Mr. Asaari Sulaiman for his kindness on providing the Sonnet® software that have been very supportive and were the main key of success for my project report. I am beholden to my colleagues for their helpful ideas, comments and suggestion towards the completion of this project report especially to Alfy Merican and Leen. Furthermore, this project has benefited from the insightful comments of the reviewers like Madam Kamariah and Aidil Saifan. Finally, my wholehearted gratitude I expressed to my beloved family for being very supportive and their thoughtful advice.

#### ABSTRACT

Discontinuities in interconnect are usually the result of change in the layout on the printed circuit board. Thus, this project is evaluated so that we can discover and observe how discontinuities such as bends in microwave integrated circuit (MICs) could contribute towards losses in microstrip line. The meander line is used as a microstrip line, and it is designed to match the 50 $\Omega$  characteristic impedance. Four substrates will be bringing into play which consists of RT/Duroid5870, RT/Duroid6010, FPC16 and FR4 so that the influence of the substrate permittivity  $\varepsilon_r$  to the discontinuity can be examined. The frequency range for the study is varied from 1GHz to 20GHz. For the microstrip design and simulation, the *Microwave Office* software will be represented. To yields such an accurate results, simulation and experiment procedure is put into practice. There are three type of bending that will be observe here which is 90° curved, 45° mitered and 120° mitered that commonly known as optimally mitered. The result will be displayed for both the scattering parameter (*S*-parameter) and the current distribution.

Keywords: Discontinuity, curved bend, mitered, chamfered, matched, optimally mitered, substrate permittivity, mismatch, reflection, transmission coefficient, S-parameter, current distribution, width to height ratio (w/h), dielectric, transmission line, edge current and longitudinal current.

iy

# TABLE OF CONTENTS

	Page
TITLE	i
DECLARATION	ii
ACKNOWLEDGEMENT	iii
ABSTRACT	iv
TABLE OF CONTENTS	v
LIST OF FIGURES	viii
LIST OF TABLES	xiii
1.0 INTRODUCTION	
1.1 Overview	1
1.2 Objectives	3
1.3 Methodology	4
2.0 MICROWAVE THEORY	5
3.0 MICROSTRIP TRANSMISSION LINE	
3.1 Introduction	12
3.2 Dispersion	16
3.3 Substrate Material	18
4.0 SOURCE OF LOSSES	ı.
4.1 Introduction	20
4.2 Conductor Loss	21
4.3 Dielectric Loss	22
4.4 Radiation Loss	23

### 5.0 DISCONTINUITIES

5.1 Introduction	24
5.2 Foreshortened Open-Circuit	25
5.3 Coupling Gap	26
5.4 Short-Circuits Through Ground Plane.	27
5.5 Step-Width Changes	28
5.6 T-Junctions	28
5.7 Bends	30
6.0 DESIGN PROCEDURE	
6.1 Introduction	35
6.2 Substrate Identification.	• 35
6.3 Physical Dimensional Layout.	37
6.4 Microstrip Test Circuit.	38
7.0 SIMULATION	
7.1 Introduction	41
7.2 Theoretical Results.	44
7.3 Simulation Results	47
7.3.1 Input Reflection Coefficient, Magnit	tude $S_{11}$ . 48
7.3.1.1 RT/Duroid5870	49
7.3.1.2 RT/Duroid6010	50
7.3.1.3 FPC16	
7.3.1.4 FR4	52
7.3.1.5 Straight Line	53
7.3.1.6 Curved Bend	54
7.3.1.7 Mitered Bend	55
7.3.1.8 Optimally Mitered Bend	56
7.3.2 Transmission Coefficient, Magnitude	e S <sub>21</sub> . 57
7.3.2.1 RT/Duroid5870	57
7.3.2.2 RT/Duroid6010	59
7.3.2.3 FPC16	60