# DC TO DC CONVERTER: BUCK-BOOST CONVERTER

This thesis is presented in partial fulfillment for the award of the Bachelor of the

Electrical Engineering (Hons.)

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#### ACKNOWLEDGEMENT

First and foremost, I wish to express my deep gratitude to my supervisor, Dr. Mohammad Nawawi Seroji, for all his valuable guidance, assistance and support throughout my studies at the Universiti Teknologi MARA. He is the one who introduced me to this field and helped me to understand this technology.

I also wish to thank Prof. Madya Dr. Ahmad Maliki Bin Omar for his helps and guidance and my friend Mr. Mahazan Bin Soh for his ideas and assistance in this project. Besides that, I wish to thank to all of my friends especially to final year degree student in electrical engineering for their suggestions and support on this project. Their comments on this project are greatly appreciated.

Most importantly I would like to extend my gratitude to my parents for their support, patience and assurance during my pursuit for higher studies. They have encouraged me throughout my education, and I will always be grateful for their sacrifice, generosity and love.

Finally yet importantly, thanks to all the persons who directly or indirectly contributed because their perspective and guidance helped greatly to point me in the right direction until the completion of this thesis.

٧

#### ABSTRACT

This thesis presents Buck-Boost converter. This is a type of DC converter that can either step up or step down voltage input. It is a class of switching-mode power supply (SMPS) containing at least two semiconductor switches (a diode and a transistor) and at least one energy storage element.

The characteristic and performance of the buck-boost converter was proved with the results and findings from conducted experiment with the Buck-Boost converter model and the simulation. Circuit simulation has been carried out using PSIM software 7.1.2 version. Result from theoretical, simulation experiment and has been compared to each others.

# TABLE OF CONTENT

CHAPTER	CONTENTS	PAGE
	Declaration	iii
	Dedication	iv
	Acknowledgment	v
	Abstract	vi
	Table of Contents	vii
	List of Figures	ix
	List of Tables	X
CHAPTER 1	INTRODUCTION	1
1.	l Introduction	1
1.1	2 Objective	2
1.	3 Scope of Work	2
1.4	4 Research Methodology Flowchart	3
CHAPTER 2	LITERATURE REVIEW	4
2.	I Introduction of Buck-Boost converter	4
2.2	2 Principle of operation	5
2	3 Continuous Mode	6
2.4	4 Discontinuous Mode	8
2.:	5 Limit between continuous and discontinuous modes	10
CHAPTER 3	CIRCUIT SIMULATION	13
3.	1 Duty ratio at 0.018 (step down)	13
3.	2 Duty ratio at 0.1 (step up)	15

#### **CHAPTER 1**

#### **INTRODUCTION**

#### **1.1 Background**

The three most common single-transistor, unisolated DC to DC converter are introduced and analyzed under assumption of ideal, lossless circuit components; transistor, diode, inductor, capacitor and load resistor. The three topologies have similar circuit waveforms which under the assumption of a large output smoothing capacitor, may be considered to be piece-wise linear, that is rectangular, trapezoidal or triangular.

Furthermore, the converters have similar inductive switching characteristics. At transistor turn on the devise current must rise to its full circuit value before the devise voltage can fall from its off-state level, and conversely at turn off the devise voltage must rise to its off state level before the devise current can fall

The characteristic of the three converters are examined in each two operating mode; continuous current inductor current and discontinuous inductor current. In the former mode the inductor current is always continuous while in the latter the inductor current falls to zero during the transistor off-period

Throughout the following analysis the transistor is assumed to operate with a fixed frequency of period T, and the transistor on-time to period ratio or duty ratio (D) is denoted. The circuit components are assumed lossless and the output ripple is very small.

1