PROTOTYPE OF QUADRATURE AMPLITUDE MODULATION (QAM) BASEBAND MODEM ON A DIGITAL BASEBAND SIGNAL PROCESSOR

RUZAIMAH BINTI MAHMUD

A Dissertation Submitted to Faculty of Electrical Engineering in Fulfillment of the Requirement for Bachelor of Engineering (Hons) Electrical UNIVERSITI TEKNOLOGI MARA MALAYSIA

> FACULTY OF ELECTRICAL ENGINEERING UNIVERSITI TEKNOLOGI MARA 40450 SHAH ALAM, SELANGOR MAY 2011

ACKNOWLEDGEMENT

This dissertation would not have been possible without the guidance and the help of several individuals who in one way or another contributed and extended their valuable assistance in the preparation and complete of this study.

All praise is to Allah S.W.T, The Most Gracious and The Most Merciful that has given me the strength, ability and patient to complete this Final Year Project.

First and foremost, I am heartily thankful to my supervisor, Puan Roslina Mohamad, whose sincerity and encouragement I will never forget. She has been my inspiration in the completion this research work.

I would like to thank my colleagues and staffs in the Electrical Engineering for use the facilities in the Microcontroller Lab.

I owe my deepest gratitude to my mother, my family and all those who supported me in any respect during the completion of this project, the omnipresent God, for answering my prayers for giving me the strengths to plod on despite my research, thank you so much Dear Lord.

Thank you.

ABSTRACT

This project presents the prototype of Digital baseband modem on Digital Signal Processing Kit. The research focuses on different modulation schemes which are 16-QAM, 32-QAM, 64-QAM, 128-QAM and 256-QAM.All these modulation are schemes simulated by using MATLAB® R2008/a Simulink. The main objective of this project is to implement the QAM baseband modem. The research continues with developing M-file MATLAB® in order to get the BER graph and at the same time to evaluate the performance of M-ary QAM system. In the implementation part, simulink model of M-ary QAM is implemented on Digital Signal Processing (DSP) Starter Kit TMS320C6713. This implementation corporate with Code Composer Studio (CCS) installed in host Personal Computer (PC). The entire simulink model is uploaded through CCS and complied into DSK.

Keywords - Addictive White Gaussian Noise(AWGN);Digital Baseband Signal Processing (DBSP);Bit Error Rate (BER);Quadrature Amplitude Modulation (QAM)

TABLE OF CONTENTS

ACKNOWLEDGEMENT	i
ABSTRACT	ii
TABLE OF CONTENT	iii
LIST OF FIGURES	v
LIST OF TABLES	vii
LIST OF SYMBOLS AND ABBREVIATIONS	vii
CHAPTER1:INTRODUCTION	1
1.1 INTRODUCTION INTO DIGITAL SIGNAL PROCESSING	1
1.2 OBJECTIVE	3
1.3 SCOPE OF WORK	3
1.4 ORGANIZATION OF THESIS	4
CHAPTER 2: LITERATURE REVIEW	5
2.1 QUADRATURE AMPLITUDE MODULATION (QAM	5
2.2 CONSTELLATION PLOTS	8
2.3 APPLICATION OF QAM	12
2.4 ADVANTAGES AND DISADVANTAGES OF QAM	13
2.5 NOISE INTERFERENCE	13
2.6 BIT ERROR RATE (BER)	14
2.7 ENERGY PER BIT-TO-NOISE DENSITY RATIO (E_b/N_o)	15
2.8 CHANNEL CODING	15
2.9 DIGITAL SIGNAL PROCESSO	16
2.10 BLOCK DIAGRAM OF DSP SYSTEM	19

CHAPTER 1

INTRODUCTION

In this chapter, an introduction into digital signal baseband communication signal processing is explained. At the end of this chapter, the objectives, scope of-works are discussed and the organization of this thesis is explained.

1.1 INTRODUCTION INTO DIGITAL SIGNAL PROCESSING

Baseband signal processing plays as fundamental both in selecting reserve digital communication system architecture and deciding the necessary computation speed of all involved algorithms. The block diagram of typical digital communication system or digital signal processing functions are shown in Figure 1.1.The upper blocks, consists of information source, source encoder, channel encoder, and modulator, prescribe the signals information from the source to the transmitter. Meanwhile transmitter prescribes the signals information from the receiver back to the source; where the lower blocks is a reversible function of signal processing steps by the upper blocks. Of all the signal processing steps, modulation and demodulation functions are essential in baseband signals processing functions[1].

The channel deterioration causes errors in the received signal. Channel encoder is integrated in the system to add redundancy to the information sequence in order to reduce the transmission errors. The primary goal of error control coding is to maximize the dependability of transmission[1].