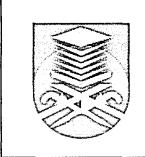


74141 FTR 20 6201 64100 200118445

DESIGN OF MULTILAYER BANDPASS FILTER FOR WIFI APPLICATION

Thesis presented in partial fulfillment for the award of the Bachelor of Electrical Engineering (Hons) UNIVERSITI TEKNOLOGI MARA



ZAINAL FITRI BIN ABDUL SAMAD FACULTY OF ELECTRICAL ENGINEERING UNIVERSITI TEKNOLOGI MARA 40170 SHAH ALAM SELANGOR

ACKNOWLEDGEMENT

In the name of God, the most gracious, the most compassionate. First and foremost I would like to express profound gratitude to my supervisor, Pn. Robi'atun Adayiah Bt Awang, for his invaluable support, encouragement, supervision and useful suggestions throughout this project work. His moral support and continuous guidance enabled me to complete my work successfully.

No words can express my gratitude to my family for all they have given me through my entire life and academic especially my parent, Encik Abdul Samad bin Inin and Puan Siti Fatimah bt Mohamad. I would not make it this far without their support, patience and encouragement.

My sincere appreciation also goes to Maizatun bt Muhammad for her willingness sharing knowledge and information with me upon completing this project. Also thanks to all my project team-mates who are directly or indirectly give their support and assistance from starting until finishing this project.

ABSTRACT

In this project, a multilayer stripline interdigital-hairpin bandpass fliters for wifi application has been proposed. By using the multilayer configuration technique in microwave filter design, it will effectively reduce the size, increase the bandwidth and eventually increase the performance of the filters. The design of four-pole double layer and six-pole triple layer of bandpass filters using the proposed structures are provided to see the comparison between the two filters. The center frequency of both multilayer bandpass filters are set to 2.4 GHz which is equivalent to wifi application. The filters are designed using Chebyshev filter with passband ripple of 0.01dB. Based on the results obtained the fractional bandwidth of four-pole double layer bandpass filter and the six-pole triple layer bandpass filter are 41% and 83% respectively. The Rogers RT/Duroid 5880 with a 0.508 mm substrate thickness and a relative permittivity of 2.2 is used to build the filter. The simulation of this multilayer bandpass filters was performed using CST Microwave Studio simulator software.

TABLE OF CONTENTS

DECLARATION	iii
ACKNOWLEDGEMENT	iv
ABSTRACT	v
TABLE OF CONTENTS	vi
LIST OF FIGURES	viii
LIST OF TABLES	xi
LIST OF ABBREVIATION	xii

CHAPTER	ę	PAGE
1 INTRODU	JCTION	
1.1	BACKGROUND	1
· 1.2	PROBLEM STATEMENT	1
1.3	OBJECTIVE	2
1.4	SCOPE OF THE PROJECT	2
1.5	THESIS ORGANIZATION	3
		н.
2 LITERAT	URE REVIEW	
2.1	MICROSTRIP CIRCUIT	4
	2.1.1 THE QUASI-TEM APPROXIMATION	5
	2.1.2 EFFECTIVE DIELECTRIC CONSTANT ANI	D
	CHARACTERISTIC IMPEDANCE	6
	2.1.3 SUBSTRATE MATERIALS	8
	2.1.4 LOSSES IN MICROSTRIP	8
2.2	SCATTERING PARAMETER (S-PARAMETER)	9
2.3	MICROWAVE FILTERS	11
	2.3.1 FILTER THEORY	11
	2.3.2 TYPES OF FILTER	13