

**PHOTOLUMINESCENCE STUDY OF ZINC OXIDE (ZnO)  
NANOSTRUCTURED BY SOL-GEL IMMERSED METHOD**

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**Final Year Project Report Submitted in Partial Fulfillment of the  
Requirements for the Degree of Bachelor of Science (Hons.)  
Physics in the Faculty of Applied Sciences  
Universiti Teknologi MARA Malaysia**

**MAY 2010**

## ACKNOWLEDGEMENT

With a deep sense of gratitude, I wish to express my sincere thanks to my supervisors, Prof. Dr. Saifollah Bin Abdullah and both of my co-supervisor Prof. Madya Dr. Rusop Mahmood and Dr. Syed Yusaini, for their immense help in planning and executing the works in time. The confidence and dynamism with which Prof. Dr. Saifollah Bin Abdullah guided the work requires no elaboration. His company and assurance at the time of crisis would be remembered lifelong. On the other hand, profound knowledge and timely wit came as a boon under the guidance of Prof. Madya Dr. Rusop Mahmood and Dr. Syed Yusaini. Their valuable suggestions as final words during the course of work are greatly acknowledged. What I know today about the process of research, I learned from Prof. Dr. Saifollah Bin Abdullah, Prof. Madya Dr. Rusop Mahmood and Dr. Syed Yusaini.

The episode of acknowledgement would not be complete without the mention of my friend from Bachelor of science (Hons) Physics for their timely help to me during complete this proposal.

I also want to thank my parents, who taught me the value of hard work by their own example. I would like to share this moment of happiness with my mother, father, brothers and sisters. They rendered me enormous support during the whole tenure of my proposal.

Finally, I would like to thank all whose direct and indirect support helped me completing my proposal in time.

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## ABSTRACT

Nanostructured Zinc oxide (ZnO) were grown on silicon (Si) substrates through sol-gel immersed method and spin-coating technique with presence of gold as catalyst. ZnO then were annealed at temperature of 400°C, 500°C and 600°C and it found that annealing temperature had improved the crystalline structure of ZnO. The ZnO nanostructures were characterized using photoluminescence (PL) spectrofluorometer and X-ray diffraction (XRD) for morphology and optical properties study.

**Keyword: Zinc Oxide (ZnO) nanostructure, room temperature, Photoluminescence (PL), X-ray Diffraction (XRD), Sol-Gel Immersed Method**

# CHAPTER 1

## INTRODUCTION

### 1.1 Background and problem statement

Zinc oxide is a unique material that exhibits semiconducting and piezoelectric dual properties. <sup>[1]</sup> ZnO has a long history of usage for pigments and protective coatings on metals. The electrical, optoelectronic <sup>[1]</sup> and photochemical properties of undoped ZnO has resulted in use for solar cells, transparent electrodes and blue/UV light emitting devices. <sup>[2]</sup>

Compared with other semiconductor materials, Zinc oxide (ZnO) has a wide band gap II–VI semiconductor ( $E_g \sim 3.37$  eV) and has been extensively investigated as an ideal candidate for optoelectronic devices such as light-emitting diodes and lasers. Especially, its large exciton binding energy ( $\sim 60$  meV) allows efficient excitonic emission even at room temperature, thus brings promise for low-threshold and high-efficiency photonic devices.<sup>[3]</sup> It also finds applications ranging from transparent electrodes in solar cells <sup>[3]</sup>, gas sensors <sup>[3][4]</sup>, varistors <sup>[4]</sup>, spintronic devices , to surface acoustic wave devices .<sup>[4]</sup> ZnO also has more resistant to radiation, and is multifunctional with uses in the areas as a piezoelectric, ferroelectric and ferromagnetic. ZnO-based semiconductor and nanowire devices are also promising for the integration on a single chip. So far, the various