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FINAL REPORT OF DIPLOMA PROJECT

DIGITAL CLOCK AND AZAN READER

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ABSTRACT

This project is probably using many ICs. All the IC is from the TTL family IC. So our project consists and concludes the large schematic and pattern layout. Our project is different from the other digital clock that available in the market because all of the function of the clock (including the alarm and other features) are all integrated into one low-power chip about a millionth of a watt. That chip is embedded directly into the circuit (tiny blob of black plastic) powered by low voltage while our circuit is powered by 5V DC supply.

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1. INTRODUCTION

1.1 Background

Time management is important for every body. For many years, the people using the device that indicates the time called 'clock'. However, over last several decades, electronics have found extensive use in much design such as in electrical appliances, calculators, computers, machines, TV's, game and other applications due to their low current requirement and small size. Their usage has become widespread in designing in clock that we know as digital or electronic clock. Recent development in digital technology enable to lead the generating of time setting that could required us to set the desired time (Azan reader).