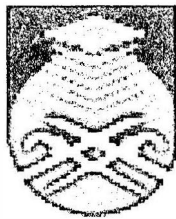


**THE PERFORMANCE OF BLOCK CODES IN DIGITAL
COMMUNICATION SYSTEM**

**This thesis is presented in partial fulfillment for the award of the
Bachelor of Electrical Engineering (Honours)
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ABSTRACT

Error control coding incorporates information into the signal that allows a receiver to find and correct bit error occurring in transmission and storage. Since such coding detects or corrects errors incurred in the communication or storage channel, it is often referred to as channel coding. [1]

This project involves the design of a complete digital communication system. The objective is to first compare the performance of different type of channel without block code. The channels used are AWGN channel, Multipath Rayleigh channel and Rician Fading channel. The second objective is to evaluate the performance of different type of block codes using AWGN channel. AWGN channel is selected because AWGN channel is a good model for satellite and deep space communication link. It is not a good for most terrestrial links because of multipath, terrain blocking, interference, and other. However for terrestrial path modeling, AWGN is commonly used to simulate background noise of the channel, in addition to multipath, terrain blocking, interference, ground clutter and self interference that modern radio systems encounter in terrestrial operation. The block codes that are covered are Hamming code and BCH code. For every different block codes the n -bit and k -bit parameters vary.

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CHAPTER 1

INTRODUCTION

1.1 DIGITAL COMMUNICATION SYSTEM

A typical communication system incorporating coding is shown in Figure 1. Error control coding is applied after the source information is converted into digital format by the source encoder. The separation between coding and modulation is conversational, although it will be found later that there are two must be design together. At the receiver, the operations are carried out in reverse order relative to the transmitter [2].

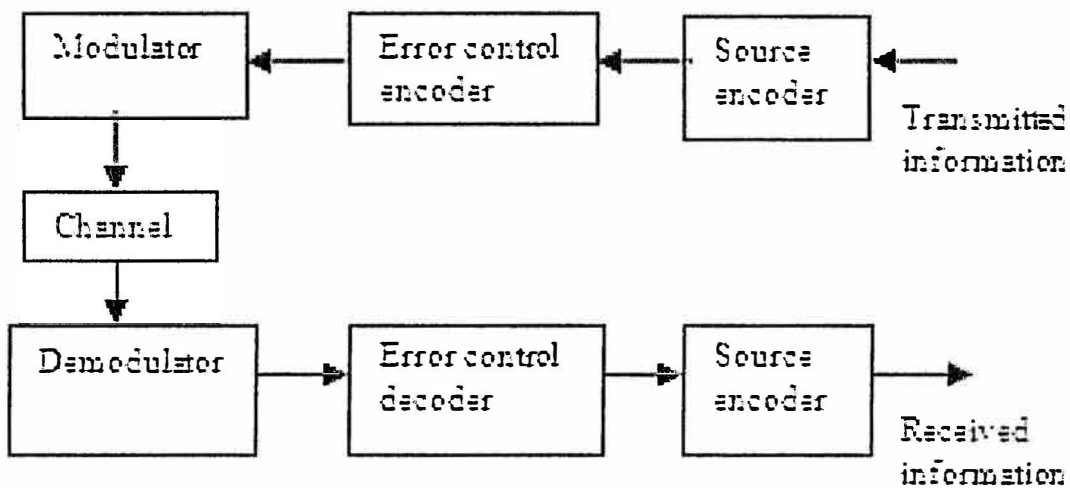


Figure 1: Coded communication system

Forward error control (FEC) devices are often located close to the receiver of an analog signal, in the first stage of digital processing after a signal has been received. That is, FEC circuits are often an integral part of the analog-to-digital conversion process. Many FEC coders can also generate a bit-error rate (BER) signal which can be used as feedback to fine-tune the analog receiving electronics. The maximum fraction of errors that can be corrected is determined in advance by the design of the code, so different forward error