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FINAL REPORT OF DIPLOMA REPORT

AMPLIFIER TIMER

DATE: MARCH 2003

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ABSTRACT

This circuit turn-off an amplifier or any other device when a low level audio fed to its input is absent for 15 minutes at least.

Pushing P1 the device is the switched-on feeding any appliance connected to SK1. Input audio signal is boosted and squared by IC2 A & B and monitored by LED D4. when D4 illuminates, albeit for very short peak, IC3 is reset and the restarts its counting. Pin 2 of IC3 remains at the low state, the to transistor are on and the relay operates. When, after the 15 minutes delay, no signal appeared at the input, IC3 end its counting and pin 2 goes high. Q1 & Q2 stop conducting and the relay switches-off. The device is thus completely off as also are the appliances connected to SK1, C5 & R9 reset IC3 at the power on. P2 allows switch-off at any moment.

Acknowledgement.

In the name of Allah (s.w.t) the moat gracious and merciful.Thank for giving us the idea and oppurtunity to complete this project successful.

Firstly we like to express our deep gratitude and appeciation to our supervisor,miss Norhayati because she was helping oyr and her idea to finish our project report.We will never forget everithing especially her cooperation for us and we appreciate it a lot.

We also want to thank to other lecture who was giving to us a information to search the data of the component.

Also for our friend we canot forget to their helping .We also refer to many book to search the information where we were never get it from internet,we also go to the library to search the book about the component data.We would to thank to our friend who has helping our to make the circuit maker and the simulation because we don't knoww how to this part before this.

Because this space are limited many more individual or friend that we not list but we know who you are and we never forget for your helping.Thank you very much.

INTRODUCTION

Amplifier timer is a circuit major about Amplifier.The name "operational amplifier" or "op-amp" was choosen because this circuit was use as high gain dc amplifier performing mathematical operation.

IC op amp is vwry high gain dc amplifier that can have it's operating characteristic changed by connecting different external components.This make the op-amp very versatile and it is this versatility thet has made the op-amp the most widely used linear IC.

Timer circuit is use to turn off an amplifier or any device when a low lever audio signal Fed to its input is absent for is minutes at least.

By using this project we can setup the turn off automatic circuit with the timer circuit by using amplifier or any device so we can relax not worry about turn off the amplifier.