UNIVERSITI TEKNOLOGI MARA KAMPUS BUKIT MERTAJAM 2003

FINAL REPORT OF DIPLOMA PROJECT

FACULTY OF ELECTRICAL ENGINEERING



(EMERGENCY LIGHTNING UNIT)

ZAMZAHARI ISHAK MUHAMMAD IZAT EMIR AMINULLAH

ACKNOWLEDGEMENT

Alhamdulillah, thank to Allah because with Allah permission we were succeed to make a paper work project. Therefore, we want to thank to all people who involved and contribute their energy and time to help us.

Their contribution is very meaningful to us. Without their help it is hard to us to finish this paper work project. Hopefully all of us get right guidance a religion and blessing from Allah.

This acknowledgement dedicated to :

- Tuan Haji Mohd Noor B. Tajuddin (Supervisor)
 Lecturer in faculty Engineering
 UiTM Penang.
- 2. Our family and all of our friends .
 - " Thank You Very Much And May Allah Bless All of You "

ZAMZAHARI ISHAK

MUHAMMAD IZAT EMIR AMINULLAH

ABSTRACT.

Project 2 (KEU 380) is one of the subject that compulsory for the student in their final year of diploma in Electrical Engineering. This subject has 3 credit hours and each group must have two students. We are going to present this project at the end of the semester. In this project the student need to do the hardware circuit using printed circuit board (PCB) and folio, which in this must have details about project that we chose such as objective, explaination about the main component, simulation result,Gant chart and others.

The purpose of doing this project is to produce the students who have ability to create something new with their creativity and high skills. It also make students use all their knowledge, capability, creativity and skills to create and solve the trouble that occur in their project. It trains students for their preparation to deliver a creative ideas and good interpersonal image to future employee. Besides, student can learn how to work on the printed circuit board (PCB) on their own. They must know how to identify error and a troubleshoot when the circuit did not function. Through this project the student must explain about main component that involved in the circuit such as IC, transistor and others.

Here, students can improve their understanding regarding function of component in circuit .In the other hand, student not only have to understand the objective when built this circuit but they also must know the circuit operation and its function.

TABLE OF CONTENTS

Acknowledgement		i
Abstract		ii
CHAPTER		
1.	INTRODUCTION	
	1.1 Introduction	1
	1.2. Objective	2
2.	CIRCUIT DESIGN AND OPERATIONS	
	2.1 Circuit description	3
	2.2. Light sensing	5
	2.2.1. Schematic diagram	7
	2.2.2. List of component	8
	2.2.3. Cost of component	10
	2.3. Circuit simulation	12
	2.4. PCB design	13
3.	HARDWARE CONSTRUCTION	
	3.1. The printed circuit board (PCB) layout	14
	3.1.1. Circuit board	17
	3.1.2. PCB drawing	18
	3.1.3. Drilling	19
	3.1.4. Inserting component	19
		• /

3.1.5. Soldering3.2. Gant chart3.2. Final assembly

PAGE

INTRODUCTION.

The Emergency Lightning Unit has two sensing circuit, one of which monitors the main voltage, while the other monitors the ambient light level. The sensing circuit are connected to trigger short period monostable timers, one for each of the above two conditions.

The outputs from the monostable are connected to the inputs of a two input AND gate, and as may be seen from the Truth Table in Fig.1 the output is only (logic 1) when both inputs are also high. Thus, from the above it may be seen that the output may only be high very briefly if both the power and light fail at practically the same instant time.

The brief output from the AND gate is then used to trigger a bistable latching circuit, which once trigged remains latched until it receives a turn – off signal. The circuit therefore only switches on the emergency light if a power failure is accompainied by simultaneous reduction in light level as would occur in an occupied room at night when the power fails.

The circuit also has facilities which permit manual operation of the light as may be required when returning home in the evening after the power has already failed earlies in the day.



Fig.1 : An AND gate symbol and truth table .