# RESISTIVE SWITCHING BEHAVIOR OF SOL-GEL SPIN COATED ZINC OXIDE THIN FILMS ON ITO SUBSTRATES

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### ABSTRACT

This work focuses on the resistive switching behavior of sol-gel spin coated zinc oxide (ZnO) thin films on ITO substrate. The deposited ZnO thin films were annealed at various temperatures from 350°C to 500°C in a furnace for 60 minutes in order to study the effect of annealing temperature on the memristive behavior of ZnO thin film. The electrical property of the thin film was characterized using 2-point probe current-voltage (I-V) measurement. The surface morphology and film thickness were examined and measured using AFM and surface profiler respectively. It was found that the samples were followed the memristive behavior as shown in I-V measurement graph.

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### **CHAPTER 1**

### INTRODUCTION

### **1.1 BACKGROUND**

In 1971, the memristor was originally founded by circuit theorist Leon Chua as a missing non-linear passive two -terminal electrical component relating electric charge and magnetic flux linkage [1]. He also stated that more recently argued that the memristor definition could be generalized to cover all forms of 2-terminal non-volatile memory devices based on resistance switching effects [2] although some experimental evidence contradicts this claim since a non-passive "nanobattery" effect is observable in resistance switching memory [3]. He also urged that the memristor is the oldest known circuit elements with its effects on the resistor, capacitor and inductor [4].

Nowadays, the memristor is under development by various teams including Hewlett-Packard, SK Hynix, and HRL Laboratories. The memristor has a special behaviour, when current flows in one direction through a memristor, the electrical resistance increases and when current flows in the opposite direction, the resistance decreases. When the current is stopped, the memristor retains the last resistance that it had, and when the flow of charge starts again, the resistance of the circuit will be what it was when it was last active [5]. The memristor device described by HP is said to have a regime of operation with an approximately linear charge-resistance relationship as long as the time-integral of the current stays within certain bounds [6].