The Phase-Change Memory with a Separate-Heater Layer Design For The Multilevel Storage Device

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ABSTRACT

A phase-change memory structure with a separate heater layer was proposed to enable crystallization process for multilevel storage. This is to overcome the problem faced by the conventional design which is the difficulties to control the crystallization process in order to achieve the multilevel storage. A finite element analysis was conducted to investigate the possibility of multilevel storage using COMSOL 4.4 Multiphysics software. A 100ns SET pulses with an increasing amplitude from 0.1V to 0.7V were applied for heating the memory layer, which is Ge₂Se₂T₅ (GST). The transition from the amorphous to the crystalline phases induced by heating the material above its crystallization temperature (450K-900K) and switching back to the amorphous state is realized by melting and quenching the material. The result is the proposed design shows more data to be stored in order to achieve the multilevel storage than the conventional design of phase-change memory with a 50nm thickness of memory layer (GST) and the 500nm width of the structure itself.

TABLE OF CONTENTS

DESCRIPTION		PAGE
Approval		i
Declaration		ii
Acknowledgement		iii
Abstract		iv
Table of Contains		V
List of Figure		vii
List of Table		ix
List of Abbreviations		X
CHA	APTER 1: INTRODUCTION	
1.1	Introduction	1
1.2	Problem Statement	3
1.3	Objective	3
1.4	Scope of work and Limitation	4
1.5	Significant of Study	4
1.6	Thesis Overview	5
CHA	APTER 2: LITERATURE REVIEW	
2.1	Non-volatile memory (NVM)	6
2.2	Phase-change memory (PCM)	8
2.3	Germanium Antimony Tellurium (GST)	12
2.4	Other material in phase-change memory	13
2.5	COMSOL 4.4 Multiphysics software	14

CHAPTER 1

INTRODUCTION

1.1 INTRODUCTION

The phase-change memory (PCM) is the new technology of nonvolatile memory instead of the flash memory that we are currently using now [1-3]. The phase-change memory has many advantages over the flash memory. For instance, it has fast switching speed, good data retention, high endurance, low programmable energy, excellent scalability and improved compatibility with complementary metal oxide semiconductor (CMOS) [4].

The Germanium-antimony-tellurium or GST (GeSbTe) is a phase change material from the chalcogenide glasses group that possesses highly different electrical resistivities depending on its phases. The resistance will be high in amorphous state while it will be low in crystalline states [5-6]. The GST currently is used in rewritable optical discs and also the phase-change memory applications. Its recrystallization time is about 20 nanoseconds that allowing bitrates of up to 35 Mbit per second to be written and direct overwrite capability up to 10,000,000 cycles. The new phase-change memories are possible using n-doped GeSbTe semiconductor with the melting point of the alloy is about 600 degree C and the crystallization temperature is between 100 and 150 degree C which is 450K to 900K.